BostonElectronics

VS1 Detector Set

Sensitive, fast 1x1mm LWQIR set for <2 to 11+ microns with user selectable DC or AC coupling, user selectable upper frequency 1.5MHz, 15MHz or 200MHz, and variable gain.

- PVMI-4TE-10.6-1x1
- PIP-DC-200M-F-M8
- PTCC-01-BAS

2

Photovoltaic multiple junction detectors PVM

PVM series features room temperature and TE cooled IR multiple junction photovoltaic detectors.

Detector type	Cooling, operating temperature $T[K]$	Optimal wavelength*) $\lambda_{opt}[\mu m]$	Detectivity**) $D^*\left[\frac{cm\cdot\sqrt{Hz}}{W}\right]$ @λ_{peak}	Detectivity**) $D^*\left[\frac{cm\cdot\sqrt{Hz}}{W}\right]$ @λ_{opt}	Current responsivity length product $R_i\cdot L\left[\frac{A\cdot mm}{W}\right]$	Time constant $\tau[ns]$	Resistance $R[\Omega]$	Acceptance angle $\varnothing\left[^\circ\right]_{\frac{1}{2}NA}$	Optical area***) $[mm\times mm]$	Package	Window****)
PVM	uncooled, ~300	8	≥1.2×10⁸	≥6.0×10⁷	≥0.008	≤4	50 to 300	≥90, 0.71	0.1×0.1 0.2×0.2 1×1 2×2 3×3 4×4[1)]	BNC, TO39	no window
		10.6	≥2.0×10⁷	≥1.0×10⁷	≥0.002	≤1.5	20 to 150				
	two-stage TE-cooled (2TE), ~230	8	≥6.0×10⁸	≥3.0×10⁸	≥0.015	≤4	150 to 1000	~70, 0.87		TO8, TO66	wedged ZnSe AR coated
		10.6	≥2.0×10⁸	≥1.0×10⁸	≥0.006	≤3	90 to 350				

*) *Other optimal wavelengths available upon request.*
**) *Data sheet states minimum guaranteed D* values for each detector model. Higher performance detectors can be provided upon request.*
***) *Other optical area available upon request.*
****) *Other windows available upon request.*
1) *Optical area available only for uncooled detectors.*

Photovoltaic detectors optically immersed PVMI

PVMI series features room temperature and TE cooled IR multiple junction photovoltaic detectors, optically immersed (achieved by using high refractive index micro lenses) in order to improve performance of the devices, different acceptance angle and saturation level. Both PVM and PVMI devices are optimized for the maximum performance at opt. Highest performance and stability are achieved by application of variable gap HgCdTe semiconductor, optimized doping and sophisticated surface processing.

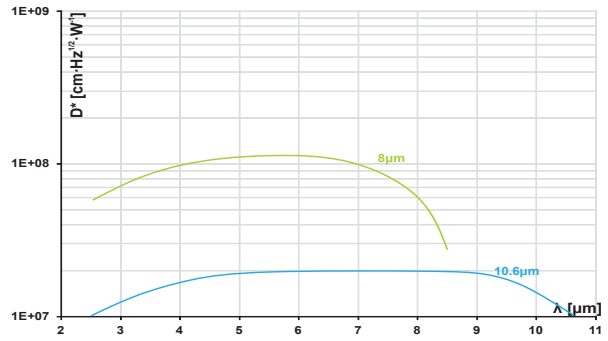
Detector type	Cooling, operating temperature $T[K]$	Optimal wavelength*) $\lambda_{opt}[\mu m]$	Detectivity**) $D^*\left[\frac{cm\cdot\sqrt{Hz}}{W}\right]$ @λ_{peak}	Detectivity**) $D^*\left[\frac{cm\cdot\sqrt{Hz}}{W}\right]$ @λ_{opt}	Current responsivity length product $R_i\cdot L\left[\frac{A\cdot mm}{W}\right]$	Time constant $\tau[ns]$	Resistance $R[\Omega]$	Acceptance angle $\varnothing\left[^\circ\right]_{\frac{1}{2}NA}$	Optical area***) $[mm\times mm]$	Package	Window****)
PVMI	uncooled, ~300	8	≥6.0×10⁸	≥3.0×10⁸	≥0.04	≤4	50 to 300	~36, 1.62	1×1 2×2	BNC, TO39	no window
		10.6	≥2.0×10⁸	≥1.0×10⁸	≥0.01	≤1.5	20 to 150				
	two-stage TE-cooled (2TE), ~230	8	≥2.5×10⁹	≥2.0×10⁹	≥0.10	≤4	150 to 1000			TO8, TO66	wedged ZnSe AR coated
		10.6	≥1.5×10⁹	≥1.0×10⁹	≥0.05	≤3	90 to 350				
	three-stage TE-cooled (3TE), ~210	8	≥4.0×10⁹	≥3.0×10⁹	≥0.15	≤4	200 to 1500				
		10.6	≥2.0×10⁹	≥1.5×10⁹	≥0.10	≤3	100 to 400				
	four-stage TE-cooled (4TE), ~195	8	≥8.0×10⁹	≥6.0×10⁹	≥0.20	≤4	500 to 2000				
		10.6	≥2.5×10⁹	≥2.0×10⁹	≥0.15	≤3	120 to 500				

*) *Other optimal wavelengths available upon request.*
**) *Data sheet states minimum guaranteed D* values for each detector model. Higher performance detectors can be provided upon request.*
***) *Other optical area available upon request.*
****) *Other windows available upon request.*

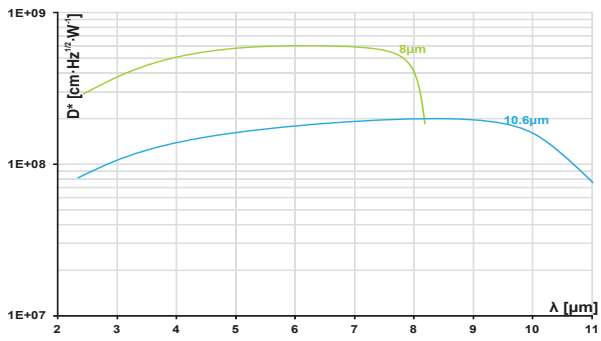



Spectral characteristics*)

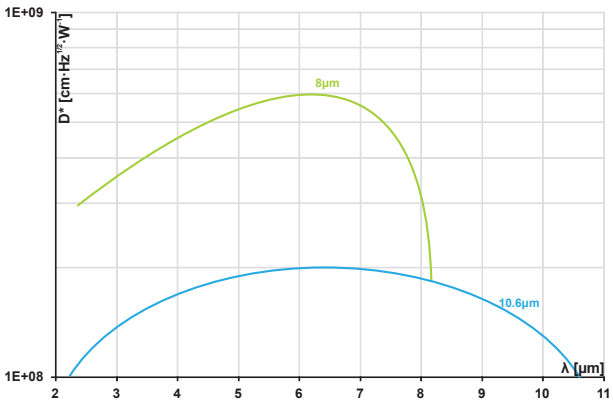
PVM



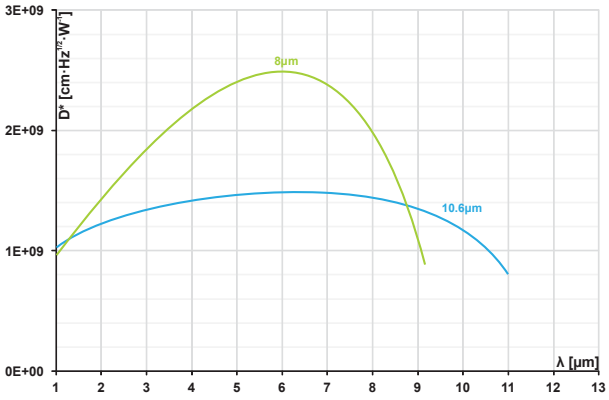
PVM-2TE



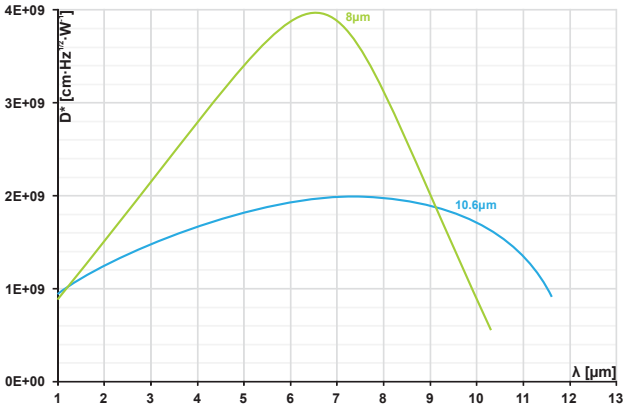
PVMI



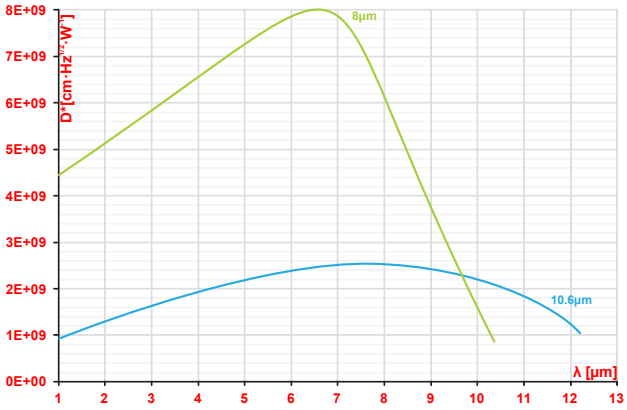
PVMI-2TE



PVMI-3TE



PVMI-4TE



*) *Example of D* vs wavelength λ for HgCdTe detectors.*
Spectral characteristics of individual detectors may vary from those shown on the chart.

PIP preamplifier

PIP is the programmable "smart" preamplifier. Due to the modern internal configuration, it offers extreme flexibility combined with superior signal parameters and high reliability. Included voltage monitor allows user to check the working conditions (supply voltages, detector bias voltage, first and last stage output voltage offset etc.)

- User may also immediately change the gain, coupling (AC/DC), optimize the first stage transimpedance (in terms of input noise and overall bandwidth), reduce the bandwidth down to 1.5 MHz (for supressing wideband noise and convenient weak signal observation), and also manually or automatically supress the voltage offset.
- The optimized parameters are immediately stored into the internal EEPROM memory and automatically loaded after the power is on.
- Reset to the factory settings is always available, and following the manual, the operation and manipulation is both: easy and safe.
- In some cases, detector biasing condition may be adjusted, however, for detector safety this function is blocked in factory by default.
- For proper operation PTCC-01 TEC controller is required.

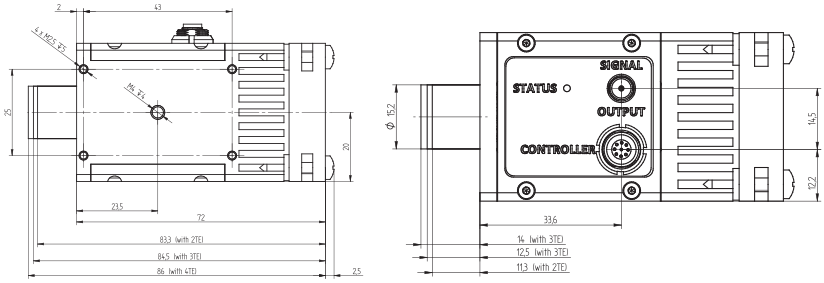
Code description

PIP - DC - 200M - F - M8 *see the preamplifier specification table for additional information*

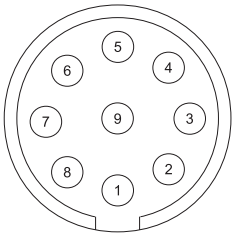
- PIP: VIGO preamplifier type
- DC: Low cut-off frequency f_{-3} [Hz]: DC/10 - configurable by software
- 200M: High cut-off frequency f_{-3}[Hz]: 20M, 200M
- F: Package: F - with fan
- M8: Mounting hole: M4 - M4 mounting hole, M8 - M8x1 mounting hole

VS1 Selectable bandwidths: 1.5MHz, 15 MHz, 200 MHz

Dimensions [mm]



Power supply and TEC control connector - LEMO connector female



Pin number	Symbol	Function
1	FAN+	FAN (+)
2	TH2	thermistor output (2)
3	TEC–	TEC supply input (–)
4	$-V_{sup}$	power supply input (–)
5	GND	power ground
6	$+V_{sup}$	power supply input (+)
7	TEC+	TEC supply input (+)
8	TH1	thermistor output (1)
9	DATA	data pin

Preamplifier type	Main feature	Detector package	Detector type	Detector cooling	Radiator, cooling, TEC controlling	Input noise voltage density	Input noise current density	Low cut-off frequency
						$e_n\left[\frac{nV}{\sqrt{Hz}}\right]$	$i_n\left[\frac{pA}{\sqrt{Hz}}\right]$	$f_{lo}[Hz]$
VIP	standalone	BNC	PV, PVI, PVM, PVMI	uncooled	not needed	0.97 – 8.0[1)]	0.02 – 3.5[1)]	DC, 10, 100, 1k, 10k
µIP	micro-size	TO39	PC, PCI, PV, PVI, PVM, PVMI	uncooled	not needed	0.97 – 8.0[1)]	0.02 – 3.5[1)]	DC, 10, 100, 1k, 10k
QIP	four-channel	TO8	PCQ, PVQ, PVMQ	uncooled	on board radiator and TEC controller, fan	0.97 – 8.0[1)]	0.02 – 3.5[1)]	DC, 10, 100, 1k, 10k
SIP	ultra-small, OEM	TO39 TO8	PC, PCI, PV, PVI, PVM, PVMI	uncooled 2TE, 3TE, 4TE	external heatsink needed	0.97 – 8.0[1)]	0.02 – 3.5[1)]	DC, 10, 100, 1k, 10k
FIP	very fast	TO8	PC, PCI, PV, PVI, PVM, PVMI	2TE, 3TE, 4TE	on board radiator, fan	1.1	5.0	1k, 10k
MIP	standard	TO8	PC, PCI, PV, PVI, PVM, PVMI	2TE, 3TE, 4TE	on board radiator, fan	0.97 – 8.0[1)]	0.02 – 3.5[1)]	DC, 10, 100, 1k, 10k
PIP	programmable	TO8	PC, PCI, PV, PVI, PVM, PVMI	2TE, 3TE, 4TE	on board radiator, fan	0.95	4.5 7.0	DC/10
AIP	on board TEC controller	TO8	PC, PCI, PV, PVI, PVM, PVMI	2TE, 3TE, 4TE	on board radiator and **TEC controller**, fan	0.97 – 8.0[1)]	0.02 – 3.5[1)]	DC, 10, 100, 1k, 10k

1) *noise measurement frequency* $f_0 = 10kHz$
2) *first stage transimpedance* $= 1k\Omega$
3) *first stage transimpedance* $= 5k\Omega$
4) *transimpedance range* $\frac{K_{imax}}{K_{imin}}$ up to 5 (dependent on f_{hi})
5) $f_{hi} \leq 1MHz$, *load resistance* $R_L = 1M\Omega t$
6) $f_{hi} > 1MHz$, *load resistance* $R_L = 50M\Omega$

High cut-off frequency	Transimpedance	Output impedance	Output voltage swing	Output voltage offset	Power supply voltage	Power supply current	Supply connector	Signal output
$f_{hi}[Hz]$	$K_i\left[\frac{V}{A}\right]$	$R_{out}[\Omega]$	$V_{out}[V]$	$V_{off}[mV]$	$V_{sup}[V]$	$I_{sup}[mA]$		
100k, 300k, 1M, 5M, 10M, 20M	fixed up to 1.0×10^5	50	±10[5] ±2[6]	max ±20[9]	±15[12] ±9[13]	max ±25	DB9	BNC
100k, 300k, 1M, 5M, 10M, 100M, 200M	fixed up to 1.0×10^5	50	±2[5] ±1[6]	max ±20[9]	±9	max ±50	MOLEX1×3	MMCX
100k, 300k, 1M, 5M, 10M, 100M	fixed up to 2.0×10^5	50	±2[5] ±1[6]	max ±20[9]	+5	max ±50	DC 2.1/5.5	**4×MCX**
100k, 300k, 1M, 5M, 10M, 100M, 250M	tunable[4] up to 1.0×10^5	50	±10[5] ±1[6]	max ±20[9]	±15[12] ±9[13]	max ±50	AMP2×4	MMCX
1G	fixed up to 8.5×10^3	50	±1	-	+12/-5	+100 -50	LEMO	SMA (**DC monitor as an option**)
100k, 300k, 1M, 5M, 10M, 100M, 250M	fixed up to 2.0×10^5	50	±10[5] ±2[7] ±1[8]	max ±20[9]	±15[12] ±9[13]	max ±50	LEMO	SMA
150k/1.5M/20M 1.5M/15M/200M	digitally adjustable 500 – 30k[2] 2.5k – 150k[3]	50	±1	max ±20[9] (DC) max ±10 (AC)	±9	typ ±80 max ±100	LEMO	SMA
100k, 300k, 1M, 5M, 10M, 100M, 250M	fixed up to 2.0×10^5	50	±2[5] ±1[6]	max ±20[9]	+5[10] +12[11]	max ±50	DC 2.1/5.5	2×SMA (**DC monitor as an option**)

7) $1MHz < f_{hi} \leq 20MHz$, *load resistance* $R_L = 1M\Omega$
8) $20MHz < f_{hi} \leq 250MHz$, *load resistance* $R_L = 50M\Omega$
9) *Measured with equivalent resistor at the input instead of the detector. It's to avoid the environmental thermal radiation's impast*
10) *with uncooled,* 2TE *and* 3TE *detectors*
11) *with* 4TE *detectors*
12) $f_{hi} \leq 1MHz$
13) $f_{hi} > 1MHz$

PTCC-01 – Programmable "smart" TEC controller

PTCC-01 is the programmable, precision, low noise, thermoelectric cooler controller, intended to operate with VIGO IR detection modules. It is compatible with both classic (MIP, SIP, FIP) and new, programmable PIP preamplifiers.

Available options:

PTCC-01-OEM

- TE C controller with built-in power supply, without housing
- configurable by PC software
- status LED indicator and status/data connector

PTCC-01-BAS

- TEC controller with built-in power supply, encapsulated in a small package
- configurable by PC software
- status LED indicator

PTCC-01-ADV

- TEC controller with built-in power supply, encapsulated in a small package
- configurable by built-in function keys or PC software
- user interface: LCD and buttons

Specification

Parameter	Value
Temperature stability [K]	±0.01 (T_{det}=233K (-40°C), T_{det}=0.1K)
Temperature readout stability [mK]	max 1 (T_{det}=233K (-60°C), T_{det}=0.1K)
Detector temperature settling time [s]	25 (T_{det}=233K (-40°C), T_{det}=0.1K) 45 (T_{det}=233K (-60°C), T_{det}=0.1K) 60 (T_{det}=233K (-80°C), T_{det}=0.1K)
Maximum TEC current [A]	1.2 (2TE) 0.45 (3TE) 0.45 (4TE)
Output voltage range [V]	min 3, max 14.5
Output current of the built-in power supply [mA]	±200 (output voltage: 3…14.5V)
Power supply voltage V_{sup} [V]	min 9, max 16 (wider range available upon request)
Power supply current I_{sup} [mA]	500 (I_{TEC}=0.45A, U_{TEC}=7.5V)
Series resistance of the connecting cable [mΩ]	1000 (total resistance of the wires supplying TEC element)
Storage temperature [°C]	from -20 to +70
Ambient temperature [°C]	from +5 to +45
Relative humidity [%]	from 10 to 90 (from +5°C to +35°C) from 10 to 50 (>+35°C)

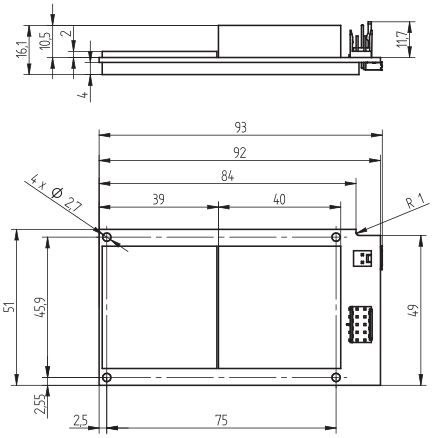
Code description

PTCC-01-BAS

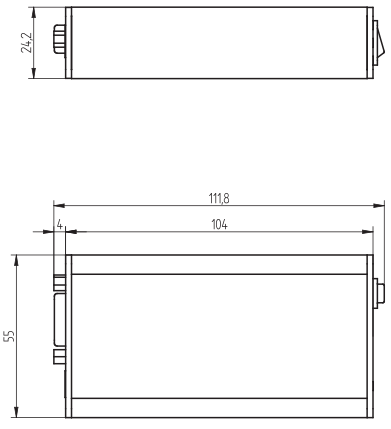
- VIGO thermoelectric cooler controller
- Version:
  - OEM - without package
  - BAS - Basic - with package
  - ADV - Advanced - with package, function buttons and LCD

Dimensions [mm]

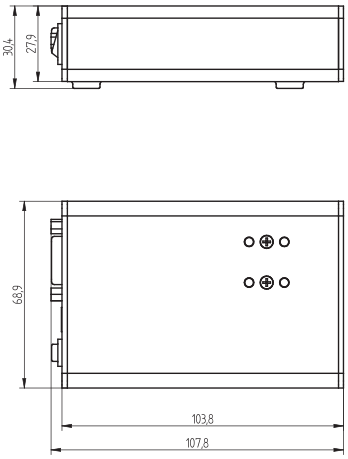
PTCC-01-OEM



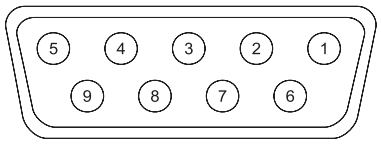
PTCC-01-BAS



PTCC-01-ADV

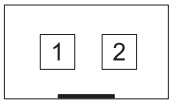


Power supply and control connector (PTCC-01-BAS and PTCC-01-ADV) - DB9 connector female



Pin number	Symbol	Function
1	TEC+	TEC supply output (+)
2	TEC–	TEC supply output (–)
3	GND	power ground
4	TH1	thermistor input (1)
5	TH2	thermistor input (2)
6	$-V_{sup}$	power supply output (–)
7	+5V	FAN and programmable preamp internal logic auxiliary supply
8	DATA	bidirectional data port
9	$+V_{sup}$	power supply output (+)
metal cover	GND-SH	shield

Power supply connector (PTCC-01-OEM) - KK2 connector male

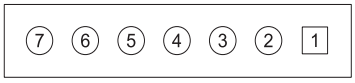


Pin number	Symbol	Function
1	TECC+	TEC controller supply input (+)
2	TECC GND	TEC controller power ground

Control connector (PTCC-01-OEM) - DUBOX2x5 connector male

Pin number	Symbol	Function
1	TEC+	TEC supply output (+)
2	TEC–	TEC supply output (–)
3	GND	power ground
4	TH1	thermistor input (1)
5	TH2	thermistor input (2)
6	$-V_{sup}$	tower supply output (–)
7	+5V	FAN and PIP preamp internal logic auxiliary supply
8	DATA	bidirectional data port
9	$+V_{sup}$	power supply output (+)
10	GND-SH	shield

Status/DATA connector (PTCC-01-OEM) - Pin Header 1x7



Pin number	Symbol	Function
1	ERR – LED	error indicator
2	LOCK – LED	temperature control loop lock indicator
3	SUP – LED	module power supply on indicator
4	3.3 V	auxiliary supply
5	TXD	transmitted data (RS-232)
6	GND	common (signal) ground (RS-232)
7	RXD	received data (RS-232)